

**WEST**[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)**Search Results -**

Term	Documents
SIMULTANEOUS.DWPI,TDBD,EPAB,JPAB,USPT.	293141
SIMULTANEOU.DWPI,TDBD,EPAB,JPAB,USPT.	3736
(22 AND SIMULTANEOUS).USPT,JPAB,EPAB,DWPI,TDBD.	0

**Database:**

☐ US Patents Full-Text Database  
☐ JPO Abstracts Database  
☐ EPO Abstracts Database  
☐ Derwent World Patents Index  
☐ IBM Technical Disclosure Bulletins

**Refine Search:**[Clear](#)**Search History****Today's Date: 9/7/2000**

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT,JPAB,EPAB,DWPI,TDBD	simultaneous and l22	0	<a href="#">L25</a>
USPT,JPAB,EPAB,DWPI,TDBD	l23 same processor same (independent or simultaneous) same access	33	<a href="#">L24</a>
USPT,JPAB,EPAB,DWPI,TDBD	(allocate or allocat??? or partition or partition???) same memory same (bank or block)	4609	<a href="#">L23</a>
USPT,JPAB,EPAB,DWPI,TDBD	5784582.pn.	3	<a href="#">L22</a>
USPT,JPAB,EPAB,DWPI,TDBD	5999197.pn.	2	<a href="#">L21</a>
USPT,JPAB,EPAB,DWPI,TDBD	116 and (Fig. 32)	0	<a href="#">L20</a>
USPT,JPAB,EPAB,DWPI,TDBD	116 and (Fig.32)	0	<a href="#">L19</a>
USPT,JPAB,EPAB,DWPI,TDBD	116 and (Fig. adj 32)	0	<a href="#">L18</a>

USPT,JPAB,EPAB,DWPI,TDBD	116 and Fig. 32	0	<a href="#">L17</a>
USPT,JPAB,EPAB,DWPI,TDBD	6097404.pn.	1	<a href="#">L16</a>
USPT,JPAB,EPAB,DWPI,TDBD	112 and 16	83	<a href="#">L15</a>
USPT,JPAB,EPAB,DWPI,TDBD	113 not 111	4	<a href="#">L14</a>
USPT,JPAB,EPAB,DWPI,TDBD	112 same 16	24	<a href="#">L13</a>
USPT,JPAB,EPAB,DWPI,TDBD	(processor or master) same (distribute or distribut??? or provide or provid??? or transfer or transfer???? or send??? or send) same clock same (processor or slave or target or peripheral or I/O)	10369	<a href="#">L12</a>
USPT,JPAB,EPAB,DWPI,TDBD	110 same 16	20	<a href="#">L11</a>
USPT,JPAB,EPAB,DWPI,TDBD	processor same (provide or provid??? or transfer or transfer???? or send??? or send) same clock same (processor or peripheral or I/O)	8216	<a href="#">L10</a>
USPT,JPAB,EPAB,DWPI,TDBD	rocessor same (provide or provid??? or transfer or transfer???? or send??? or send) same clock same (processor or peripheral or I/O)	0	<a href="#">L9</a>
USPT,JPAB,EPAB,DWPI,TDBD	17 same (arbitrate or arbitrat???)	46	<a href="#">L8</a>
USPT,JPAB,EPAB,DWPI,TDBD	(synchronous adj3 memory) or SDRAM or SLDRAM	3567	<a href="#">L7</a>
USPT,JPAB,EPAB,DWPI,TDBD	11 same 15	417	<a href="#">L6</a>
USPT,JPAB,EPAB,DWPI,TDBD	(synchronous same memory) or SDRAM or SLDRAM	15332	<a href="#">L5</a>
USPT,JPAB,EPAB,DWPI,TDBD	synchronous same memory	14729	<a href="#">L4</a>
USPT,JPAB,EPAB,DWPI,TDBD	processor sme (provide or provid??? or transfer or transfer???? or send??? or send) same clock same (processor or peripheral or I/O)	0	<a href="#">L3</a>
USPT,JPAB,EPAB,DWPI,TDBD	master same clock	18450	<a href="#">L2</a>
USPT,JPAB,EPAB,DWPI,TDBD	shared same memory	20685	<a href="#">L1</a>